



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: DEBASHIS BHATTACHARYA et al.

Serial No.: 09/896,059

For: METHOD FOR AUTOMATED DESIGN OF INTEGRATED
CIRCUITS WITH TARGETED QUALITY OBJECTIVES USING
DYNAMICALLY GENERATED BUILDING BLOCKS

Filed: June 29, 2005

Examiner: Thompson, A. M.

Art Unit: 2825

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AMENDMENT UNDER RULE 116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
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Sir:

In reply to the final Office Action dated February 9, 2006, please enter the following amendments in the consideration of the above patent application.

Amendment to the claims begins on page 2.

Remarks begin on page 17.